

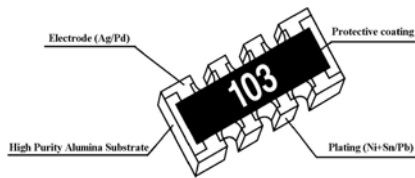
CHIP ARRAY

Features

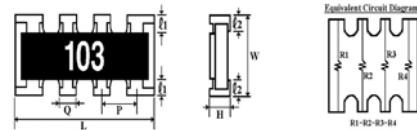
- High density, 4, 8 resistors in one small case
- Improvement of placement efficiency
- Packaging is suitable for automatic placement machines
- Superior solderability



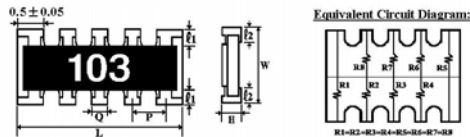
Dimension (mm)



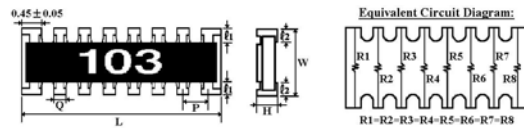
- 08P4 (8Pin 4R)



- 10P8 (10Pin 8R)



- 16P8 (16Pin 8R)



Part No.	Style	L	W	H	ℓ ₁	ℓ ₂	P	Q
08P4	08P4 (8Pin 4R)	3.2 ± 0.2	1.6 ± 0.2	0.5 ± 0.1	0.3 ± 0.15	0.3 ± 0.15	0.8 ± 0.1	0.5 ± 0.15
16P8	16P8 (16Pin 8R)	4.0 ± 0.2	1.6 ± 0.15	0.45 ± 0.1	0.3 ± 0.15	0.4 ± 0.15	0.5 ± 0.05	0.3 ± 0.05
10P8	10P8 (10Pin 8R)	3.3 ± 0.1	1.65 ± 0.1	0.6 ± 0.1	0.4 ± 0.1	0.45 ± 0.1	0.64 ± 0.05	0.35 ± 0.05

Rating

Part No.	Style	Power Rating at 70°C	Max. Working Voltage	Max. Overload Voltage	Dielectric With-standing Voltage	Operated Temp. Range	Resistance Range		Jumper Rated Current
							F (±1%) E-96 series	G (±2%) J (±5%)	
08P4	08P4 (8Pin 4R)	1/16W	50V	100V	500V	-55°C~+125°C	100Ω~560KΩ	10Ω~1MΩ	1A
16P8	16P8 (16Pin 8R)	1/16W	50V	100V	100V	-55°C~+125°C	-	10Ω~1MΩ	-
10P8	10P8 (10Pin 8R)	1/32W	25V	50V	50V	-55°C~+125°C	-	33Ω~100KΩ	-

Note: Part number and ordering procedure the same as chip resistors.

CHIP ARRAY

Performance Specifications:

Characteristics	Test Methods	Limits															
Temperature coefficient JIS - C - 5202 5.2	Natural resistance change per temp. degree centigrade $\frac{R_2 - R_1}{R_1 (t_2 - t_1)} \times 10^6 \text{ (PPM / } ^\circ\text{C)}$ R ₁ : Resistance value at room temperature (t ₁) R ₂ : Resistance value at room temp. plus 100°C (t ₂)	<table border="1"> <tr> <td rowspan="2">± 5%</td> <td>(08P4) 10 Ω --- 10MΩ ±400 PPM/°C</td> </tr> <tr> <td>(16P8, 10P8) ±200PPM/°C</td> </tr> </table> ± 1% 100Ω --- 560KΩ ≤ ±200 PPM/°C	± 5%	(08P4) 10 Ω --- 10MΩ ±400 PPM/°C	(16P8, 10P8) ±200PPM/°C												
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	(16P8, 10P8) ±200PPM/°C																
Short-time overload JIS - C - 5202 5.5	Permanent resistance change after the application of a potential of 2.5 times RCWV for 5 seconds.	± (2.0% + 0.1Ω) Max.															
Insulation resistance JIS - C - 5202 5.6	Apply 500V DC between protective coating and termination for 1 min, then measure.	1,000 Mega ohm or more (10P8) 10,000 Mega Ohm or more															
Dielectric withstanding voltage JIS - C - 5202 5.7	Apply 500V AC between protective coating and termination for 1 minute.	No evidence of flashover mechanical damage, arcing or insulation break down.															
Terminal bending JIS - C - 5202 6.1.4	Twist of Test Board: Y / X = 5 / 90 mm for 10 seconds.	± (1.0% + 0.05 Ω) Max.															
Soldering Heat JIS - C - 5202 6.4	Dip the resistor into a solder bath having a temperature of 260°C ± 5°C and hold it for 10 ± 1 seconds.	Resistance change rate is ± (1.0%+0.05 Ω)															
Solderability JIS - C - 5202 6.5	Test temperature of solder 235°C ± 5°C. Dipping them in solder: 3 ± 0.5 seconds.	95% coverage Min.															
Temperature cycling JIS - C - 5202 7.4	Resistance change after continuous five cycles for duty cycle specified below: <table border="1" style="margin-top: 10px;"> <thead> <tr> <th>Step</th> <th>Temperature</th> <th>Time</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>-55°C±3°C</td> <td>30 mins.</td> </tr> <tr> <td>2</td> <td>Room temp.</td> <td>10 - 15 mins.</td> </tr> <tr> <td>3</td> <td>+125 °C±2 °C</td> <td>30 mins.</td> </tr> <tr> <td>4</td> <td>Room temp.</td> <td>10 - 15 mins.</td> </tr> </tbody> </table>	Step	Temperature	Time	1	-55°C±3°C	30 mins.	2	Room temp.	10 - 15 mins.	3	+125 °C±2 °C	30 mins.	4	Room temp.	10 - 15 mins.	Resistance change rate is ± (1.0%+0.05 Ω)
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4	Room temp.	10 - 15 mins.															
Load life in humidity JIS - C - 5202 7.9	Resistance change after 1,000 hours (1.5 hours "on", 0.5 hour "off") at RCWV in a humidity chamber controlled at 40°C ± 2°C and 90 to 95% relative humidity.	± (3.0% + 0.1Ω) Max.															
Load Life JIS - C - 5202 7.10	Permanent resistance change after 1,000 hours operating at RCWV, with duty cycle (1.5 hours "on", 0.5 hour "off") at 70°C ± 2°C ambient	± (3.0% + 0.1Ω) Max.															

* RCWV = Rated Continuous Working Voltage = $\sqrt{\text{Rated Power} \times \text{Resistance Value}}$